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	Class
	Subclass
	ISSUE CLASSIFICATION

Best Available Copy

PATENT NUMBER

4

U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
1P1CC3 Q.A. 17	
SCANNED	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/900945	F	714		2184	J. L. DIAZ

APPENDIX

Toshitada Saito

21

2109

Training

TIME

One-chip system large-scale integrated circuit including processor circuit and its peripheral circuits

PTO-2040
12/99

ISSUING CLASSIFICATION

Continued on Issue Slip Inside File Jacket

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____				ISSUE FEE	
				Amount Due	Date Paid
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				(Legal Instruments Examiner)	(Date)

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